



### FEATURES

- Low profile , surface mount packaging , 3.71mm height.
- Meets IEEE 802.3i specifications.
- Designs include impedance matching resistor networks , receive and transmit isolation transformers and common mode chokes for improved EMI suppression
- 235°C peak IR Reflow temperature rating.
- Operating temperature 0 ~ +70°C.
- Minimum interwinding breakdown voltage of 1500 Vrms.
- Recognized By UL 60950-1



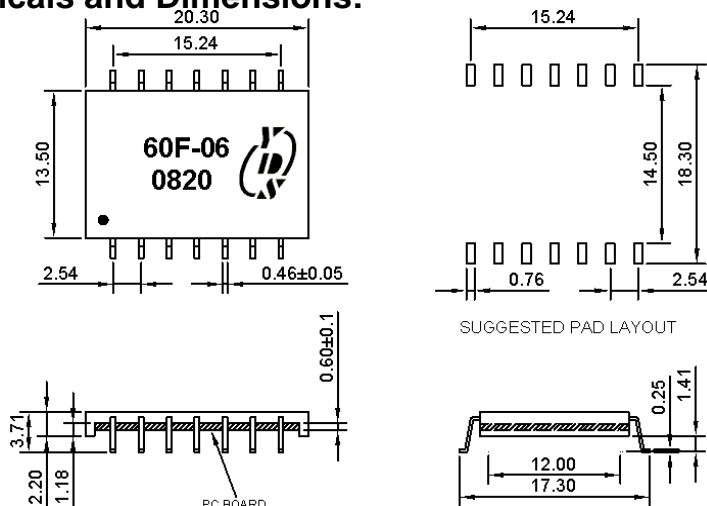
### ELECTRICAL SPECIFICATIONS @25°C-Operating temperature 0°C TO +70°C

PART NUMBER	Insertion Loss 1 to 10 MHz (dB TYP)	Attenuation (XMIT) (dB MIN)			Return Loss (dB MIN)	Crosstalk (dB MIN)	Common Mode Rejection (dB MIN)				Hipot (Vrms) @60Hz
		30MHz	50MHz	100MHz			1 to 10MHz	1-10MH Z	10MHz	50MHz	
60F-01	-5.0	-30	-35	-40	-18	-40	-60	-45	-45	-30	1500
60F-02	-5.0	-30	-35	-40	-18	-40	-60	-40	-35	-30	1500
60F-03	-5.0	-30	-35	-40	-18	-40	-60	-45	-35	-30	1500
60F-04	-5.0	-30	-35	-40	-18	-40	-60	-40	-35	-30	1500
60F-05	-5.0	-30	-35	-40	-18	-40	-60	-45	-35	-30	1500
60F-06	-5.0	-30	-35	-40	-18	-40	-60	-40	-35	-30	1500
60F-07	-5.0	-30	-35	-40	-18	-40	-60	-40	-35	-30	1500
60F-08	-5.0	-30	-35	-35	-18	-40	-60	-40	-35	-30	1500

### Resistor Table & Matched Manufacturer

Part Number	Resistor Number	Resistor Value	Manufacturer	IC Number	Application Circuit
60F-01	R1 . R3	53.6Ω±1%	AT&T	T7232	B
	R2 . R4	316Ω±1%			
	R5 . R6	49.9Ω±1%			
60F-02	NONE	N/A	Various		F
60F-03	R1 . R4	68.1Ω±1%	Symbios Logic	NCR92C02A NCR92C350	D
	R2 . R3	287Ω±1%			
	R5 . R6	49.9Ω±1%			
60F-04	R1 . R3	287Ω±1%	National	DP83902VJG DP83934	C
	R2 . R4	68.1Ω±1%			
	R5 . R6	49.9Ω±1%			
60F-05	R1 . R3	316Ω±1%	SMSC		E
	R2 . R4	53.6Ω±1%			
	R5 . R6	49.9Ω±1%			
60F-06	R1 . R4	47.5Ω±1%	Motorola	MC68160	B
	R2 . R3	N/A			
	R5 . R6	49.9Ω±1%			
60F-07	R1 . R4	200±1%	DEC	21040	A
	R2 . R3	133Ω±1%			
	R5 . R6	49.9Ω±1%			
60F-08	R1 . R3	287Ω±1%	Fujitsu		E
	R2 . R4	49.9Ω±1%			
	R5 . R6	49.9Ω±1%			

### Mechanicals and Dimensions:



### Part Number:

60F - 06 NL  
A B C

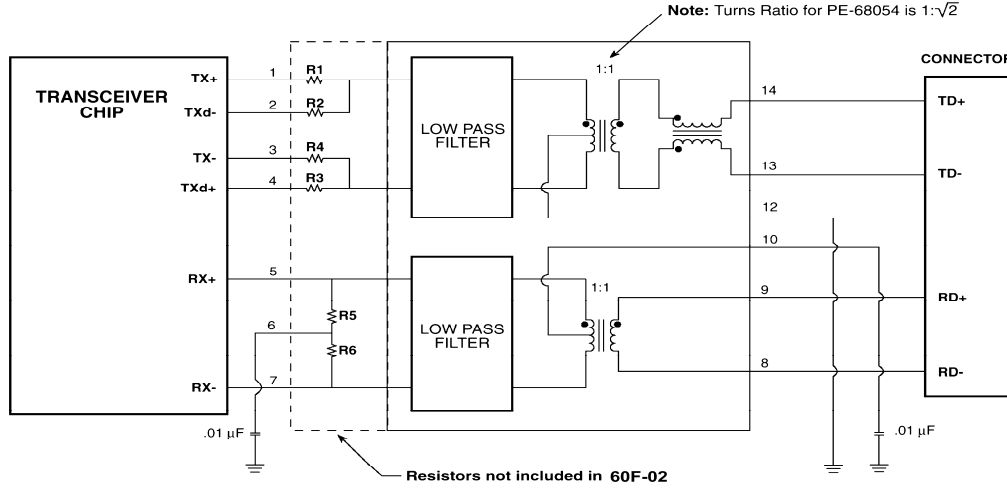
A: Series  
B: Sequence Id  
C: RoHS Version

UNIT : mm Unless otherwise specified, all tolerances are ±0.25

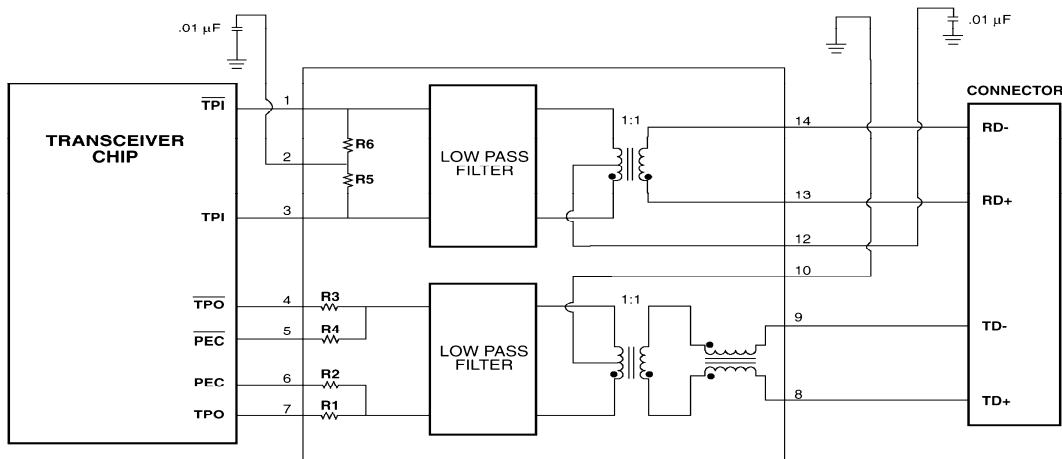


Schematics:

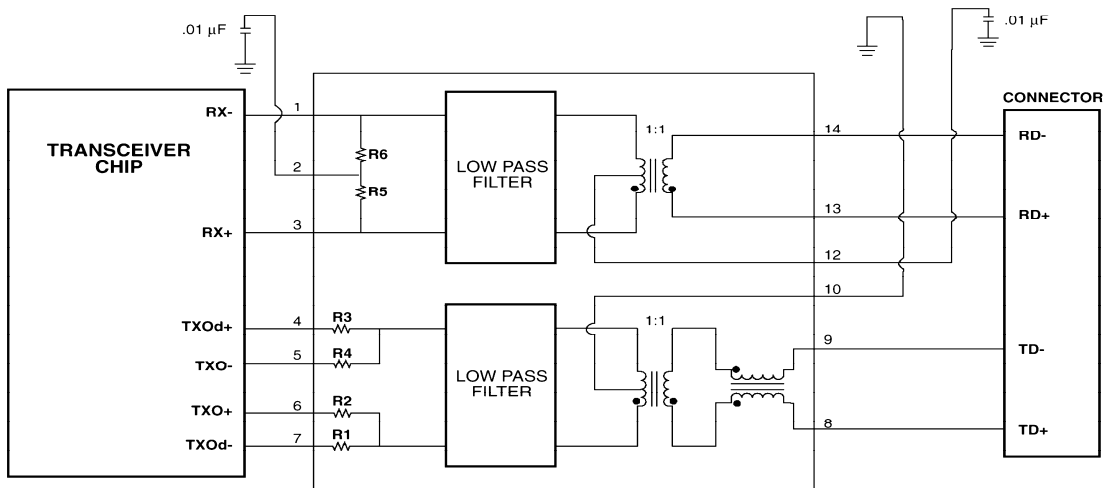
Application Circuit – A



Application Circuit – B

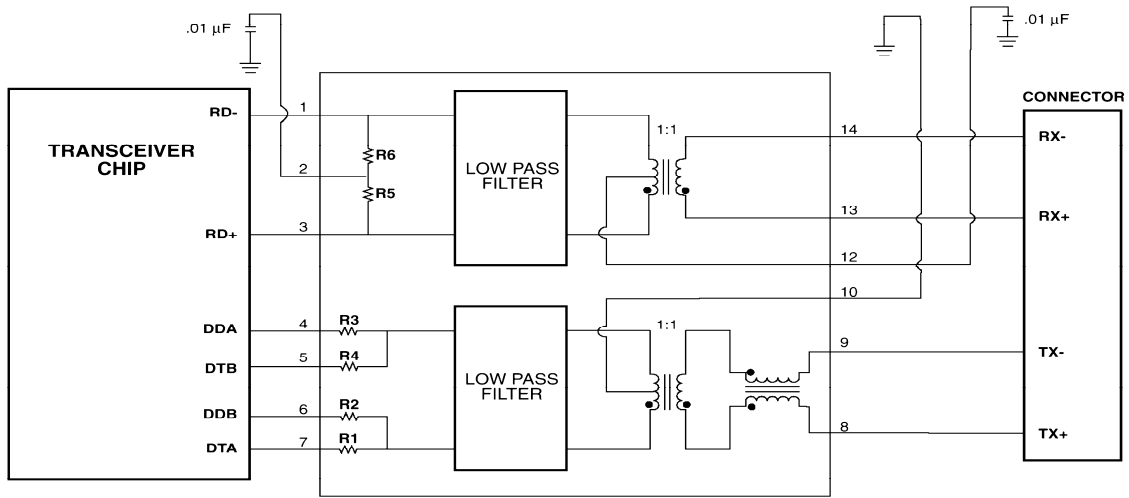


Application Circuit – C

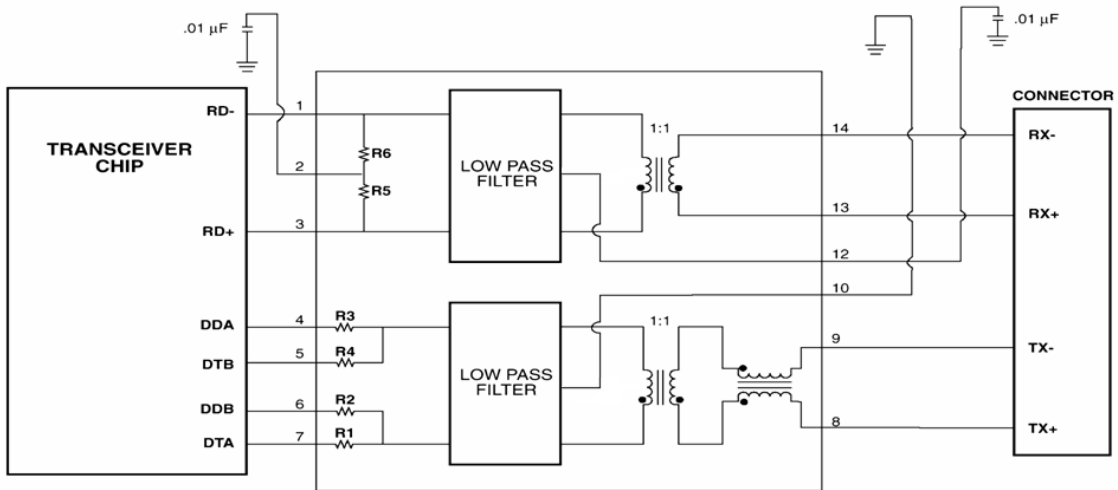




Application Circuit – D



Application Circuit – E



Application Circuit – F

